

L Number	Hits	Search Text	DB	Time stamp
-	144	circuit adj (design designing generation) and (aparatus method program tool machine device) and @pd < "19990629" and automatic and (storing recording record storage) and logic and (verification verify checking) and unit and (divide dividing) and (changing modification modify change)	USPAT	2003/12/22 11:10
-	14	"Logic Cone\$" and predetermin\$ and @pd < "19990629"	USPAT	2003/11/04 09:17
-	45	circuit adj (design designing generation) and (aparatus method program tool machine) and pd < "19990629" and automatic and (storing recording record storage save) and logic and (verification verify checking comparing) and unit and (divide dividing split\$) and (modif\$ chang\$ updat\$) and @pd>19950629 and "test\$ vector\$"	USPAT	2003/11/14 13:22
-	16	circuit adj (design designing generation) and (aparatus method program tool machine) and @pd < "19990629" and automatic and (storing recording record storage save) and logic and (verification verify checking comparing) and unit and (divide dividing split\$) and (modif\$ chang\$ updat\$) and @pd>19950629 and "test\$ vector\$"	USPAT	2003/11/14 11:26
-	60	"Logic Cone\$"	USPAT	2003/11/14 13:31
-	320	circuit adj (design designing generation) and (aparatus method program tool machine) and automatic and (storing recording record storage save) and logic and (verification verify checking comparing) and unit and (divid\$ split\$) and (modif\$ chang\$ updat\$) and @pd>"20000629"	USPAT	2003/11/14 13:48
-	26	circuit adj (design designing generation) and (aparatus method program tool machine) and automatic and (storing recording record storage save) and logic and (verification verify checking comparing) and unit and (divid\$ split\$) and (modif\$ chang\$ updat\$) and @pd>"20000629" and "test vector"	USPAT	2003/11/14 14:09
-	36	circuit\$ adj design\$ same (apparatus method program tool) and (divid\$ split\$) and (verif\$ debug\$ compar\$) and (classif\$ specif\$) and @pd < "19990629" and second and first and (point\$ area\$) and ("test vector" "testing vectors")	USPAT	2003/11/14 15:27
-	91	circuit\$ adj design\$ same (apparatus method program tool) and (divid\$ split\$) and (verif\$ debug\$ compar\$) and (classif\$ specif\$) and second and first and (point\$ area\$) and ("test vector" "testing vectors")	USPAT	2003/11/14 15:39
-	8	circuit\$ adj design\$ same (apparatus method program tool) and (divid\$ split\$) and (verif\$ debug\$ compar\$) and (classif\$ specif\$) and second and "first circuit" and (point\$ area\$) and ("test vector" "testing vectors")	USPAT	2003/12/22 07:28
-	158	"test vector" and circuit and design and (classif\$ sort\$) and (specif\$ demonstrat\$ point\$) and (model\$ specification) and (chang\$ modif\$ alter\$ revis\$ correct\$) and (unit "step" process\$)	USPAT	2003/12/22 12:50
-	134	"test vector" and circuit and design and (classif\$ sort\$) and (specif\$ demonstrat\$ point\$) and (model\$ specification descrip\$) and (chang\$ modif\$ alter\$ revis\$ correct\$) and (unit "step" process\$) and verif\$	USPAT	2003/12/22 11:37
-	53	"test vector" and circuit and design and (classif\$ sort\$) and (specif\$ demonstrat\$ point\$) and (model\$ specification descrip\$) and (chang\$ modif\$ alter\$ revis\$ correct\$) and (unit "step" process\$) and verif\$ and @pd < "19990630"	USPAT	2003/12/22 14:39
-	65	"test vector" and circuit and design and (classif\$ sort\$) and (specif\$ demonstrat\$ point\$) and (model\$ specification) and (chang\$ modif\$ alter\$ revis\$ correct\$) and (unit "step" process\$) and @pd < "19990630"	USPAT	2003/12/22 13:28
-	1	"predetermined unit" and "circuit description" and @pd < "19990630"	USPAT	2003/12/22 14:44
-	0	"predetermined unit" and ("circuit description" specification) and circuit and HDL and @pd < "19990630"	USPAT	2003/12/22 14:46
-	1309	"predetermined unit" and @pd < "19990630"	USPAT	2003/12/22 14:46
-	901	"predetermined unit" and circuit and @pd < "19990630"	USPAT	2003/12/22 15:30

-	1	"predetermined unit" and circuit and (HDL VHDL) and @pd < "19990630"	USPAT	2003/12/22 14:46
-	18	"predetermined unit" and circuit and simulation and (divid\$ alter\$ modif\$ revis\$) and @pd < "19990630"	USPAT	2003/12/22 15:27
-	336	("test vercor" "test pattern" "testing vector" "testing pattern") and circuit and simulation and (divid\$ alter\$ modif\$ revis\$) and @pd < "19990630"	USPAT	2003/12/22 15:30
-	218	("test vercor" "test pattern" "testing vector" "testing pattern") and circuit and simulation and (divide dividing divided altered altering alteration alter modification revise revising revised) and (modification modify modifying changed changing change) and (structure specification specify specified model description descriped) and @pd < "19990630"	USPAT	2003/12/22 15:34
-	169	("test vercor" "test pattern" "testing vector" "testing pattern") and circuit and simulation and (divide dividing divided altered altering alteration alter modification revise revising revised) and (modification modify modifying changed changing change) and (structure specification specify specified model description descriped) and logic and @pd < "19990630"	USPAT	2003/12/22 15:35
-	105	("test vercor" "test pattern" "testing vector" "testing pattern") and circuit and simulation and (divide dividing divided altered altering alteration alter modification revise revising revised) and (modification modify modifying changed changing change) and (structure specification specify specified model description descriped) and logic and @pd < "19990630" and @pd > "19950101"	USPAT	2003/12/22 16:49
-	46	EDA and ATPG and @pd > "19950101"	USPAT	2003/12/22 15:45
-	9	EDA and ATPG and @pd < "19990630" and @pd > "19950101"	USPAT	2003/12/22 15:49
-	9	EDA and ATPG and @pd < "19990630" and @pd > "19950101" and (test testing tested) and (modification modify modified modifying changing changed changes change alter altering altered) and circuit and (description describe descriped describing model specify specification specified)	USPAT	2003/12/22 15:53
-	144	("test vector" "test pattern" "testing vector" "testing pattern") and circuit and simulation and (divide dividing divided altered altering alteration alter modification revise revising revised) and (modification modify modifying changed changing change) and (structure specification specify specified model description descriped) and logic and @pd < "19990630" and @pd > "19950101"	USPAT	2003/12/22 16:49
-	44	("changed point" " " "modification" "changed portion" "altered point" "altered portion" modified point" " modified portion" "revised portion" "revised point") and "circuit design" and (apparatus method tool machine machanic)	USPAT	2003/12/25 21:08
-	25	("changed point" " " "modification" "changed portion" "altered point" "altered portion" modified point" " modified portion" "revised portion" "revised point") and "circuit design" and (apparatus method tool machine machanic) and @pd < "19990630"	USPAT	2003/12/25 20:04
-	25	("changed point" " " "modification" "changed portion" "altered point" "altered portion" modified point" " modified portion" "revised portion" "revised point") and "circuit design" and (apparatus method tool machine machanic) and @pd < "19990630" and ("only" "others not")	USPAT	2003/12/25 20:07
-	0	("changed point" " " "modification" "changed portion" "altered point" "altered portion" modified point" " modified portion" "revised portion" "revised point") and "circuit design" and (apparatus method tool machine machanic) and @pd < "19990630" and "others not"	USPAT	2003/12/25 20:06

-	23	("changed point" " " "modification" "changed portion" "altered point" "altered portion" modified point" " modified portion" "revised portion" "revised point") and "circuit design" and (apparatus method tool machine machanic) and @pd < "19990630" and "only" and ("test vector" "testing vector" "test pattern" "testing pattern" "node" "connection" "point")	USPAT	2003/12/25 20:54
-	18	("changed point" " " "modification" "changed portion" "altered point" "altered portion" modified point" " modified portion" "revised portion" "revised point") and "circuit design" and (apparatus method tool machine machanic) and @pd < "19990630" and "only" and ("test vector" "testing vector" "test pattern" "testing pattern" "node" "connection" "point") and unit	USPAT	2003/12/25 20:54
-	0	("changed point" " " "modification" "changed portion" "altered point" "altered portion" modified point" " modified portion" "revised portion" "revised point") and "circuit design" and (apparatus method tool machine machanic) and @pd < "19990630" and "only" and ("test vector" "testing vector" "test pattern" "testing pattern" "node" "connection" "point") and " verif\$ unit"	USPAT	2003/12/25 20:55
-	5	("changed point" " " "modification" "changed portion" "altered point" "altered portion" modified point" " modified portion" "revised portion" "revised point") and "circuit design" and (apparatus method tool machine machanic) and @pd < "19990630" and "only" and ("test vector" "testing vector" "test pattern" "testing pattern" "node" "connection" "point") and verif\$	USPAT	2003/12/25 20:56
-	5	("changed point" " " "modification" "changed portion" "altered point" "altered portion" modified point" " modified portion" "revised portion" "revised point") and "circuit design" and (apparatus method tool machine machanic) and @pd < "19990630" and "only" and ("test vector" "testing vector" "test pattern" "testing pattern" "node" "connection" "point") and verif\$ and (unit step process)	USPAT	2003/12/25 20:57
-	9	("changed point" " " "modification" "changed portion" "altered point" "altered portion" modified point" " modified portion" "revised portion" "revised point") and "circuit design" and (apparatus method tool machine machanic) and (divid\$3 partion\$3 split\$3 seperat\$3) and verif\$7	USPAT	2003/12/25 21:10